

LVDT Voltage Module

The LVM-110 Module

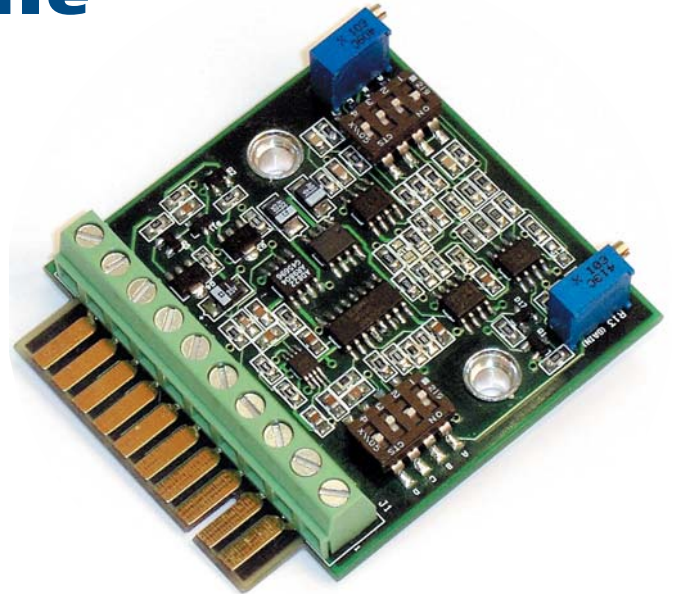
The popular **LVM-110**, low cost LVDT / RVDT signal conditioner has been re-engineered to improve performance and versatility.

The new LVM has increased LVDT drive current capability, to expand compatibility to LVDTs with lower input impedances.

The LVM-110 is designed to be a low-cost LVDT signal conditioning solution for OEM applications, and is compatible with most standard and special design Schaevitz® LVDTs.

New DIP switches replace the old, easy to lose DIP jumpers, simplifying gain and frequency setup procedure.

The new discrete circuit topology is optimized for improved signal to noise ratio and tempco, while maintaining form, fit and function backward compatibility with previous versions.



FEATURES

- ◆ Small OEM Form Factor
- ◆ Low Cost
- ◆ Six DIP Switch Selectable Gains
- ◆ 20-Turn Zero and Span Pots
- ◆ 100% Zero Suppression
- ◆ Card-Edge or Screw Termination
- ◆ Master / Slave Capability

APPLICATIONS

- ◆ Position Feedback System
- ◆ Test & Measurement
- ◆ High Density Dimensional Gaging

specifications

Power Supply Requirements:

Voltage

± 15 Vdc. $\pm 10\%$ (for ± 10 Vdc output)
 ± 12 to ± 15 Vdc. $\pm 10\%$ (for ± 5 Vdc. Output)

Current

30mA. (max)

Transducer Excitation:

Voltage

3.0 V-rms. $\pm 10\%$

Current

≤ 20 mA
(min LVDT Input impedance 150 Ω)

Frequency

2.5, 5, 8 & 10 kHz

Position Signal:

± 10 , ± 5 , 0 to 10 and 0 to 5 Vdc
(5 mA max current)

Required Signal for ± 10 Vdc Output

Minimum

100 mV rms.

Maximum

5.6 V rms.

Frequency Response

250 Hz (-3db)

Linearity

$\leq .05\%$

Tempco

$< .02\%/^{\circ}$ F

Zero Suppression

± 6 Vdc

Output Impedance

$< 1\Omega$

Noise and Ripple

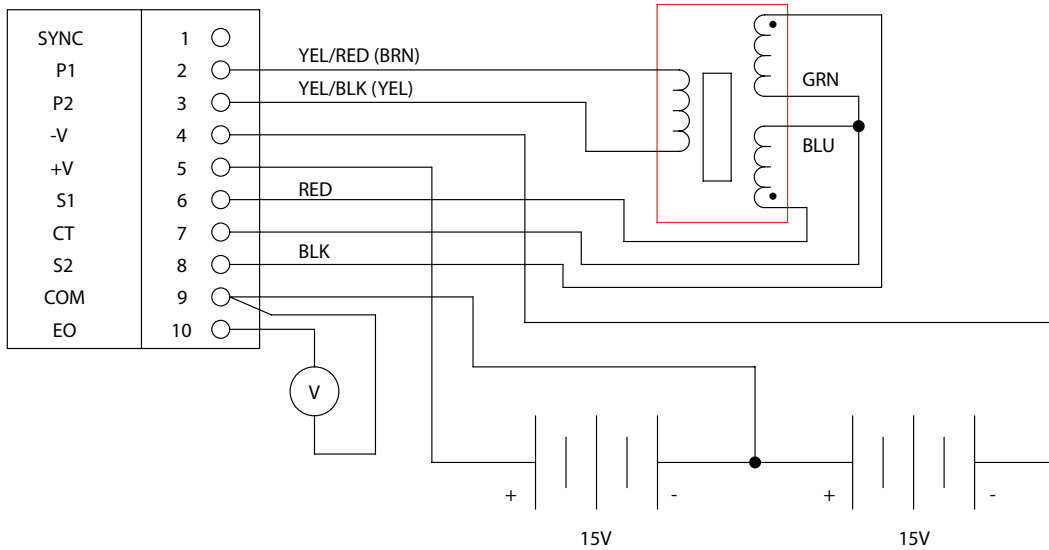
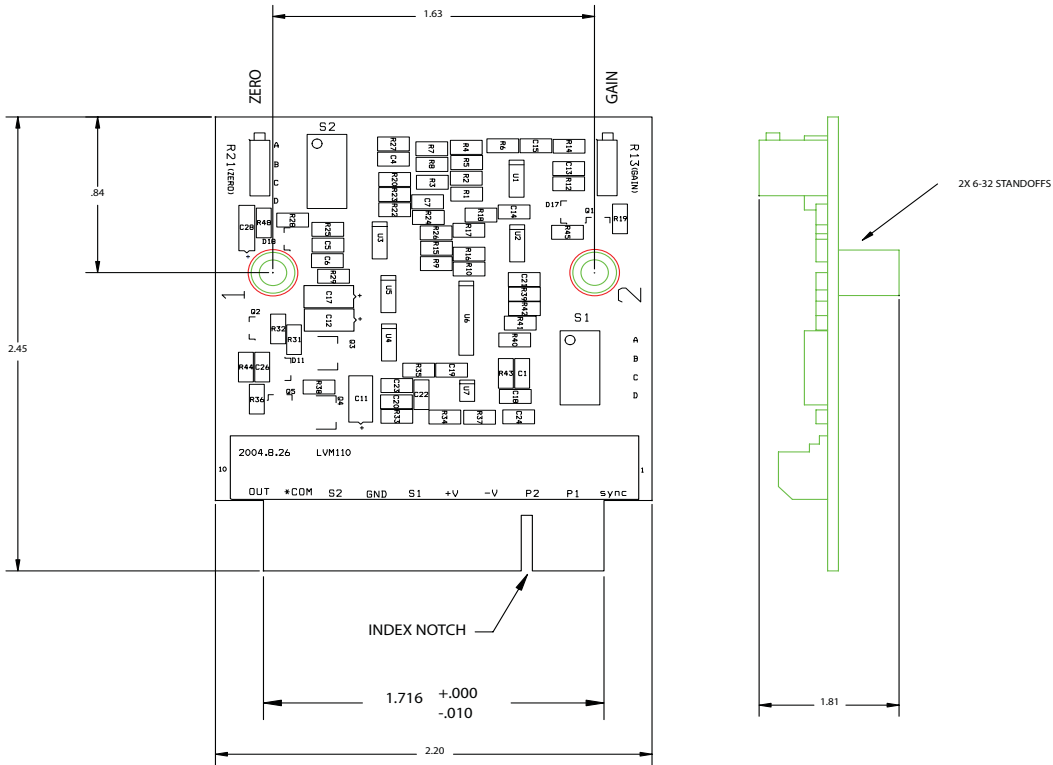
≤ 15 mV rms

Stability

.05% of FSO
(after 15 min. warm-up)

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wiring diagram



WIRING DIAGRAM